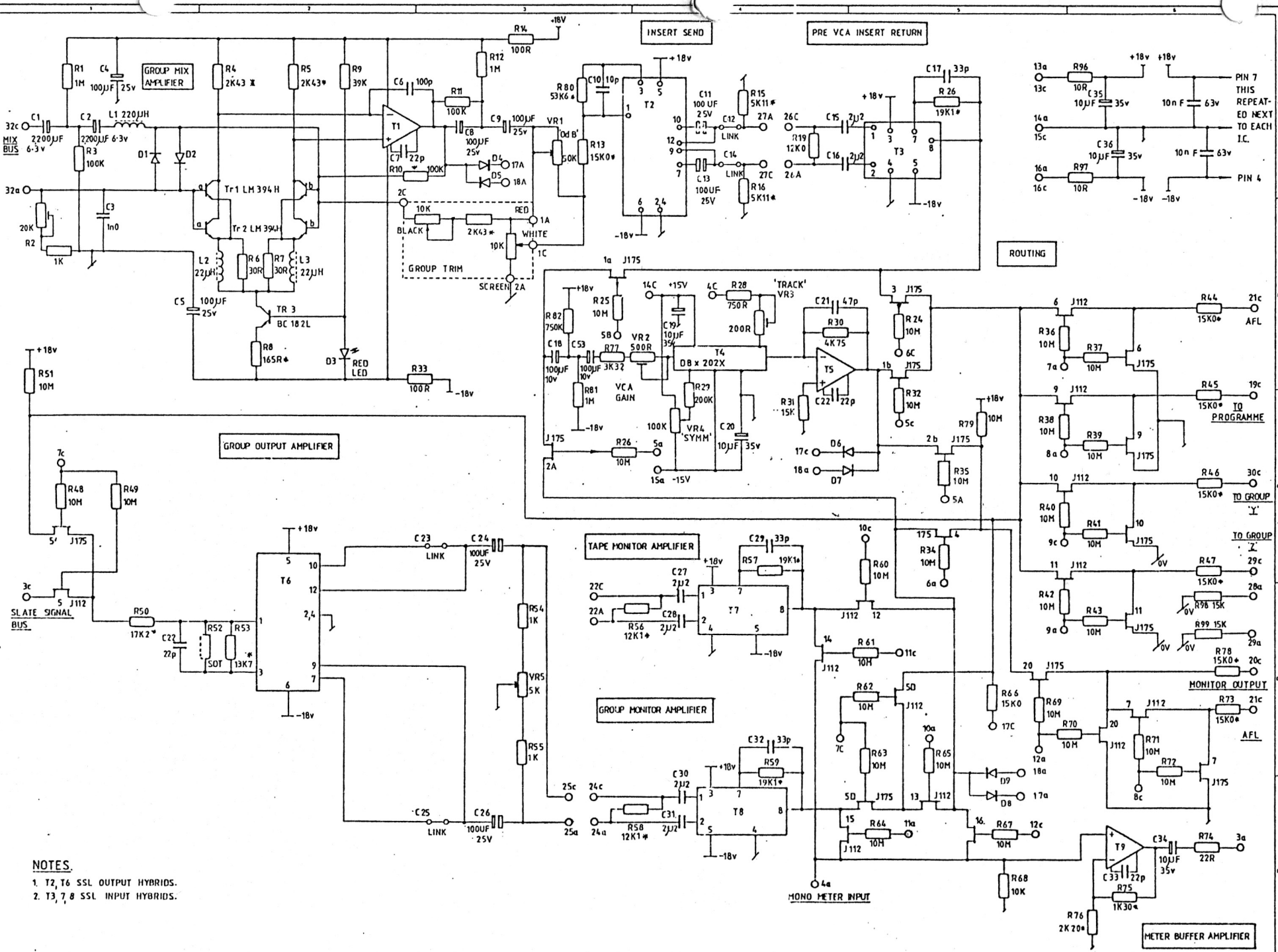




NOTES

1. T2, T6 SSL OUTPUT HYBRIDS.
2. T3, T7, T8 SSL INPUT HYBRIDS.

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Rev	Issue	Chkd	Details
			REDRAWN VB.
P	a		PROTOTYPE
2	a		R54, 55 VALUE NOW 1K VR5 VALUE NOW 5K
3	a		PL 18V ADDED AT R14
4	a		DM R2 REPLACED WITH MULTI TURN PRESET
5	a		DM FET 10 & 11 TO 0V. R98 & 99 ADDED
6	a		DM C22 ADDED
6	b		DM D4, 5 & 9 MOVED -> C1 TO GROUP A2'B' NOW Y2Z - R35 & 66 MOVED
7	a		JH C10 NOW 10p WAS 22p
8	a		DM R50 & 53 WERE 5K11 C12, 14, 23, 25 WERE 220UF. C11, 13, 24 & 25 WERE ELECT. 220UF. R80 WAS 68K1.
Title: 82256 688v L/R GROUP MIX/OUTPUT/MONITOR			
Drg. No. T82256-70			
Solid State Logic			
9-30			